

## Refine Search

### Search Results -

Term	Documents
(16 AND 18).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	38
(L18 AND L16).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	38

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### Search History

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<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L22</u>	L18 and l16	38	<u>L22</u>
<u>L21</u>	L18 and l6	307	<u>L21</u>
<u>L20</u>	L18 and l7	21	<u>L20</u>
<u>L19</u>	L18 and l6	307	<u>L19</u>
<u>L18</u>	l11 and (simd or single near2 instruction\$1)	590	<u>L18</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L17</u>	L16 and l15	12	<u>L17</u>
<u>L16</u>	(712/20-22)[CCLS]	404	<u>L16</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L15</u>	(simd or single near2 instruction\$1) and l12	133	<u>L15</u>
<u>L14</u>	L12 and l7	5	<u>L14</u>

<u>L13</u>	L12 and l6	88	<u>L13</u>
<u>L12</u>	L11 and endian\$1	220	<u>L12</u>
<u>L11</u>	L10 and (align\$8 or boundar\$4)	21564	<u>L11</u>
<u>L10</u>	vector\$1 near50 (size or length or width)	47079	<u>L10</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L9</u>	l5 and l7	5	<u>L9</u>
<u>L8</u>	l5 and l6	64	<u>L8</u>
<u>L7</u>	(711/201-221)[CCLS]	5460	<u>L7</u>
<u>L6</u>	(712/2-300)![CCLS]	11191	<u>L6</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L5</u>	L4 and (size or length or width)	136	<u>L5</u>
<u>L4</u>	L3 and (align\$8)	139	<u>L4</u>
<u>L3</u>	L2 and endian\$1	154	<u>L3</u>
<u>L2</u>	(load\$4 or stor\$4 or replicat\$3 or written or writ\$5) nearl5 vector\$1 nearl2 register\$1	4408	<u>L2</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L1</u>	5475823.pn.	1	<u>L1</u>

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IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

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Very Large Scale Integration (VLSI) Systems, IEEE Transactions on  
Volume 13, Issue 1, Jan. 2005 Page(s):1 - 13[AbstractPlus](#) | Full Text: [PDE\(768 KB\)](#) IEEE JNL**2. Access and alignment of arrays for a bidimensional parallel memory**Verdier, C.; Boutillon, E.; Lafage, A.; Demeure, A.;  
Application Specific Array Processors, 1994. Proceedings., International Conference on  
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